

# JUNKYARD ELECTRONICS

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## Prologue:

Ever seen a broken TV and got excited? Junk is not waste, if it falls in the correct hands with the right minds. So if you think you can make the best, out of the waste that we give you, register for the third version of Junkyard Electronics.

## Event Details:

- Event will be held at EDC lab in the main academic building on 31<sup>st</sup> march 2017 from 11:00 am to 12:00 am .
- It will consists of two round **Round I & Round II.**
- Round I will be circuit solving and its implementation on breadboard.
- Round II will be only circuit designing.

## Time Duration of the Event:

Approx. 1hr

## Rules and Regulations:

1. Each team should consist of two member .
2. First they have produce results on paper for round I then only they will be allowed to implement their design on breadboard.
3. If team completes round I , then only they would be allowed to enter in round II.
4. Circuit should be design using given component only.
5. Participants are not allowed to bring any electronic component like resistor.
6. **Each team is allowed to refer internet for initial 5 minutes in ROUND II only**, then it is forbidden to use internet.

\*organisers have the sole discretion to modify the rules and regulation.